PATENT NUMBER

US: Unlearly Patent Application

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្រីកើត្តិប្រើក្រុក method for a thin film semiconductor device, the thi dfilm semiconductor device itself, liquid crystal display, and electronic device

PTO-204

USSUINGICEASSIFICATION

CROSS REFERENCE(S)

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CTARNATIONAL CLASSIFICATION

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TERMINAL DISCLAIMER	DRAWINGS	CLAIMS ALLOWED				
	Sheets Drwg. Figs. Drwg. Print Fig.	Total Claims	Print Claim for O.G.			
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has been disclaimed.	(Assistant Examiner) (Date)		•			
The term of this patent shall not extend beyond the expiration date						
of U.S Patent, No.		ISSUE FEE				
	(Primary Examiner) (Date)	Amount Due	Date Paid			
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Issue	Classit	fication

Application No.	Applicant(s)							
09/885,027	MIYASAKA, MITSUTOSHI							
Examiner	Art Unit							
Jerome Jackson Jr.	2815							

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